## VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, ODISHA, BURLA

(Formerly University College of Engineering, Burla)

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No.VSSUT/TEQIP/ 117

Dated: 24/12/2013

The Institutional Technical Education Quality Improvement Programme (TEQIP) Unit of VSSUT, Burla is constituted as per the guidelines stipulated in Project Implementation Plan (PIP), MHRD, Government of India with following members:

Vice-Chancellor, VSSUT, Burla : Head of the TEQIP Unit. 1. 2 Registrar, VSSUT, Burla Member Comptroller of Finance, VSSUT, Burla 3. Member 4 Prof. S. Panda, Professor in Elect. Engg. & Nodal Officer (Financial Management Unit) Member (in place of Prof. B.B. Pati) 5. Prof. R. P. Panda, Professor in Electronics & TCE., & Nodal Officer (Academic Activities) Member 6. Prof. S. Agrawal, Professor in Electronics & TCE., & Nodal Officer (Procurement) Member Prof. P.C.Swain, Professor in Civil Engg. & Nodal Officer (Civil Works) Member Prof. P.K.Modi, Professor in Electrical Engg. & Nodal 8. Officer (Equity Assurance Plan & Monitoring Evaluation) Member 9. Er. Tapas Ranjan Mohanty, S.I, EL&TCE Deptt. Member 10. Sri L. D. Pal, Sr.Steno, Electrical Engg. Deptt. Member Sri Surendra Sahu, Peon, Mechanical Engg. Deptt. 11. Member 12. Sri Krantiraditya Dhalmahapatra, Production Engg. Deptt. 1st Year M.Tech. Student Member 13. Sri Ketan Mishra, Production Engg Deptt., 3rd Year B.Tech. Student Member 14. Dr. C.R. Mohanty, Reader in Civil Engg Deptt. & Co-coordinator TEQIP-II Co-Convener

By order of Vice-Chancellor

Memo.No:VSSUT/TEQIP/ 118

15.

Dated: 24/12/2013

Convener

Copy to:1) Person concerned for information and necessary action. 2) All HODs/Deans/Registrar/Controller of Finance for information.

Prof. D. Dhupal, Professor in Production Engg. Deptt.

3) P.A to Vice-Chancellor for kind information of the Hon'ble Vice-Chancellor.

4) TEQIP File.

and Coordinator, TEQIP-II